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David M. Pini  
838 E. Dana Street  
Mountain View, CA 94041

In re Application of  
Pini et al.

Application No. 09/994,479

Filed: 27 November, 2001

For: SIGN GENERATION BYPASS PATH TO ALIGNER FOR REDUCING SIGNED DATA LOAD LATENCY

Dear Mr. Pini:

You are named as a joint inventor in the above identified United States patent application, filed under the provisions of 35 U.S.C. 116 (United States Code), and 37 CFR 1.47(a), Rules of Practice in Patent Cases. Should a patent be granted on the application you will be designated therein as a joint inventor.

As a named inventor you are entitled to inspect any paper in the file wrapper of the application, order copies of all or any part thereof (at a prepaid cost per 37 CFR 1.19) or make your position of record in the application. Alternatively, you may arrange to do any of the preceding through a registered patent attorney or agent presenting written authorization from you. If you care to join the application, counsel of record (see below) would presumably assist you. Joining in the application would entail the filing of an appropriate oath or declaration by you pursuant to 37 CFR 1.63.

Telephone inquiries regarding this communication should be directed to the undersigned at 571/272-3231. Requests for information regarding your application should be directed to the File Information Unit at 703/308-2733. Information regarding how to pay for and order a copy of the application, or a specific paper in the application, should be directed to Certification Division at 703/308-9726 or 1-800-972-6382 (outside the Washington D.C. area).

*D Wood*

Douglas I. Wood  
Senior Petitions Attorney  
Office of Petitions

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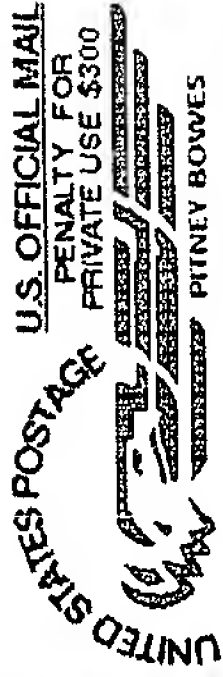
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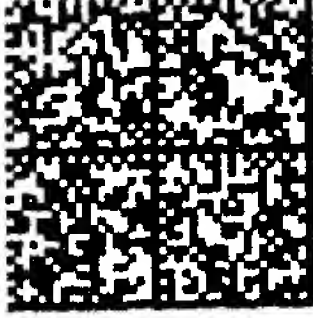
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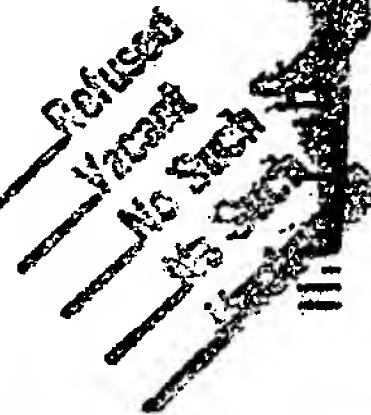
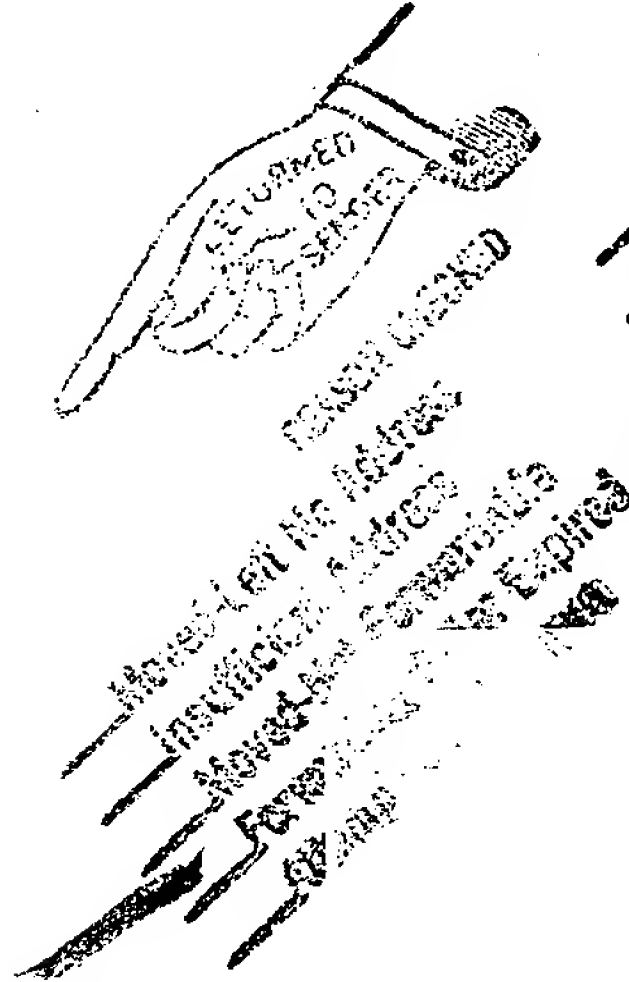
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